Sampling of Analog Signals



The variables t and n are linearly related through the sampling period T or, equivalently, through the sampling rate $F_s = 1/T$, as

$$t = nT = \frac{n}{F_s}$$

As a consequence of above equation, there exists a relationship between the frequency variable F (or Ω) for analog signals and the frequency variable f (or ω) for discrete – time signals. To establish this relationship, consider an analog sinusoidal signal of the form:

$$x_a(t) = A\cos(2\pi F t + \theta)$$

$$x_a(nT) \equiv x(n) = A\cos(2\pi FnT + \theta)$$

$$= A\cos\left(\frac{2\pi nF}{F_s} + \theta\right)$$

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The sampling Theorem

If the highest frequency contained in an analog signal $x_a(t)$ is $F_{max} = B$ and the signal is sampled at a rate $F_s > 2F_{max} \equiv 2B$ (Nyquist rate).

Example Consider the analog signal

$$x_a(t) = 3\cos 50\pi t + 10\sin 300\pi t - \cos 100\pi t$$

What is the Nyquist rate for this signal?

Solution

The frequencies present in the signal above are

 $F_1 = 25 \text{ Hz}$ $F_2 = 150 \text{ Hz}$ $F_3 = 50 \text{ Hz}$ Thus $F_{max} = 150 \text{ Hz}$ and $F_s > 2F_{max} = 300 \text{ Hz} = F_N$

Example

Consider the analog signal

 $x_a = 3\cos 2000\pi t + 5\sin 6000\pi t + 10\cos 12000\pi t$

a) What is the Nyquist rate for this signal?

b) Assume now that we sample this signal using a sampling rate $F_s = 5000$ samples/sec. what is the discrete – time signal obtained after sampling?

c) What is the analog signal y_a (t) we can reconstruct from the samples if we use ideal interpolation?



Finally, we obtain

$$x(n) = 13\cos 2\pi \left(\frac{1}{5}\right)n - 5\sin 2\pi \left(\frac{2}{5}\right)n$$

c) Since only the frequency components at 1 KHz and 2 KHz are present in the sampled signal, the analog signal we can recover is

$$y_a(t) = 13\cos 2000\pi t - 5\sin 4000\pi t$$

Quantization of Continuous – Amplitude signal

The process of converting a discrete – time continuous – amplitude signal into a digital signal by expressing each sample value as a finite (instead of an infinite) number of digits, is called quantization. The error introduced in representing the continuous – valued signal by a finite set of discrete value levels is called quantization error or quantization noise.

We denote the quantizer operation on the samples x(n) as Q[x(n)] and let $x_q(n)$ denote the sequence of quantized samples at the output of the quantizer. Hence

$$x_q(n) = Q[x(n)]$$

Then the quantization error is a sequence e_q (n) defined as the difference between the quantized value and the actual sample value. Thus

$$e_q(n) = x_q(n) - x(n)$$

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A unipolar quantizer deals with analog signals ranging from 0 volt to a positive reference voltage, and a bipolar quantizer has an analog signal range from a negative reference to a positive reference. The notations and general rules for quantization are:

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$$\Delta = \frac{(x_{\max} - x_{\min})}{L}$$
$$L = 2^{m}$$
$$i = round\left(\frac{x - x_{\min}}{\Delta}\right)$$
$$x_q = x_{\min} + i\Delta, \text{ for } i = 0, 1, \dots, L - 1$$

Where:

 X_{max} and X_{min} : are the maximum and minimum values, respectively, of the analog input signal x.

- **L:** denotes the number of quantization levels.
- **m:** is the number of bits used in ADC.
- Δ : `is the step size of the quantizer or the ADC resolution.
- **Xq:** indicates the quantization level.
- i: is an index corresponding to the binary code.

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Example:

Assuming that a 3-bit ADC channel accepts analog input ranging from 0 to 5volts, determine the following:

- **a.** Number of quantization levels.
- **b.** Step size of the quantizer.
- c. Quantization level when the analog voltage is 3.2 volts.
- **d.** Binary code produced by the ADC.

Solution:

a-no.of quantization levels =
$$2^{no.of bits}$$

no.of quantization levels = $2^3 = 8$ levels

b- Since the range is from 0 to 5 volts and the 3-bit ADC is used, we have $X_{min} = 0$ volt, $X_{max} = 5$ volts, and m= 3 bits.

$$\Delta = \frac{5-0}{8} = 0.625$$
 volt.

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c- We determine quantization level when the analog voltage is 3.2 volts.

$$i = round\left(\frac{x - x_{\min}}{\Delta}\right) = round(5.12) = 5.$$

$$x_q = 0 + 5\Delta = 5 \times 0.625 = 3.125$$
 volts.

d- The binary code is determined as 101.